

(Autonomous College Affiliated to University of Mumbai)

SECOND HALF - 2019

S.E.(SEM. IV) ESE THEORY EXAMINATION TIME TABLE

(MU Non CBGS Syllabus with Equivalent Courses from KJSCE2014)

Candidates appearing for the above examinations are requested to be present at the examination hall, fifteen minutes before the commencement of the examinations and will not be allowed after the half an hour of the commencement of the examinations. The candidates are required to carry the hall ticket / college Identity Card. THEY ARE FORBIDDEN TO CARRY ANY ELECTRONICS GADGETS LIKE MOBILE PHONE, SMART WATCH, ETC., ANY BOOKS, NOTES OR PAPER ETC. IN THE EXAMINATION HALL. Seat numbers will be displayed on the college notice boards before the date of commencement of the examination and seating arrangement will be displayed on college notice boards on the day of examination. The

ESE examinations of S.E.(SEM - IV) will be conducted as per the following schedule: -

DAY	TUESDAY 10 Dec 2019	THURSDAY 12 Dec 2019	SATURDAY 14 Dec 2019	TUESDAY 17 Dec 2019	THURSDAY 19 Dec 2019
TIME COMPUTER ENGINEERING	2.30PM-5.30 PM Computer Graphics	2.30PM-5.30 PM Analysis of Algorithm Equivalent for Analysis of Algorithm & Design	2.30PM-5.30 PM Relational Database Management Systems Equivalent for Database Management System	2.30PM-5.30 PM Analog & Digital Communication	2.30PM-5.30 PM Applied Mathematics IV Equivalent for Applied Mathematics-IV
ELECTRONICS ENGINEERING	Electronics Circuits Analysis & Design Equivalent for Electronics Circuit Analysis & Design	Digital System Design-II		Basics of Communication Engineering Equivalent for Basics of Analog & Digital Communication Systems	Applied Mathematics IV Equivalent for Advanced Engineering Mathematics

Candidates are permitted to use the scientific but not programmable calculator at the examination. Candidates for the examination are expected to provide themselves with Data books and drawing instruments if required. The exchange or loan of Data books, drawing instruments or any other materials used in the examination is not permitted while the examination is in progress. Candidates must bring their own instruments and will not be allowed to borrow from each other under any circumstances. Any unfair means in the examination hall during the theory examination is punishable as per the rules and regulations.

Courses printed in bold wherein there is no equivalent given

Mumbai - 400 077

DATE: - 5 November 2019



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	TUESDAY	THURSDAY	SATURDAY	TUESDAY	THURSDAY
DAY DATE TIME ELECTRONICS AND FELECOMMUNICAT ON ENGINEERING	10 Dec 2019 2.30PM-5.30 PM Analog Electronics II Equivalent for Electronic Devices & Circuits II	12 Dec 2019 2.30PM-5.30 PM	14 Dec 2019 2.30PM-5.30 PM Electromagnetic Field Theory Equivalent for Electromagnetic	17 Dec 2019 2.30PM-5.30 PM	19 Dec 2019 2.30PM-5.30 PM Applied Mathematics IV Equivalent for Applied Mathematics-IV
INFORMATION TECHNOLOGY	Data Communication & Networking Equivalent for Networking Technology For Digital Devices	Principles of Communication Engineering	Internet Programming	Financial Accounting & Management of Technology Innovation	Applied Mathematics IV Equivalent for Applied Mathematics-IV

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	TUESDAY	THURSDAY	SATURDAY	TUESDAŸ	THURSDAY
DAY DATE 10 Dec 2019 TIME 2.30PM-5.30 PM MECHANICAL ENGINEERING Thermal Engineering	12 Dec 2019 2.30PM-5.30 PM Theory of Machines - I Equivalent for	14 Dec 2019 2.30PM-5.30 PM Production Process- II	17 Dec 2019 2.30PM-5.30 PM Industrial Electronics	19 Dec 2019 2.30PM-5.30 PM Applied Mathematics IV	
		Theory of Machines - I	Equivalent for Production Process- II	Equivalent for Industrial Electronics	Equivalent for Applied Mathematics IV

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DAY	TUESDAY		
DATE	3 December 2019		
TIME	2.30-5.30 PM		
ELECTRONICS ENGINEERING	Electrical Machines and Instruments (of Sem III)		
	Equivalent for Electronic & Electrical Measuring Instruments & Machines		

DAY	WEDNESDAY
DATE	27 November 2019
TIME	2.30PM-5.30 PM
COMPUTER ENGINEERING	Operating System (of SEM V) Equivalent for Operating system

DAY	FRIDAY	
DATE	13 December 2019	
TIME	2.30PM-5.30 PM	
INFORMATION	Microcontroller and Embedded	
TECHNOLOGY	Systems (of Sem VI)	
	Equivalent for	
	Microprocessors & Microcontrollers	

DAY	WEDNESDAY	7 December 2019 2.30-5.30 PM	
DATE	27 November 2019		
TIME	2.30-5.30 PM		
ELECTRONICS AND TELECOMMUNIC ATION ENGINEERING	Communication Systems (of Sem V) Equivalent for Principle of Communication Engineering	Integrated Circuits (of Sem V) Equivalent for Analog & Digital IC Design & Applications	

DAY	SATURDAY		
DATE	30 November 2019		
TIME	2.30-5.30 PM		
MECHANICAL NENGIEERING	Materials Science and Metallurgy (of		
	Sem III)		
	Equivalent for		
	Material Technology		

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